



B.I.T. SINDRI, DHANBAD, JHARKHAND

DEPARTMENT OF HIGHER TECHNICAL EDUCATION , GOVERNMENT OF JHARKHAND



Internship on MULTISIM for designing analog and digital circuits.
Duration 8 week from 1st june to 31 july 2025

1.Introduction and purpose:

In this internship, Analog as well as Digital circuit implementation will be taught. Here, Students will be able to gain knowledge on Analog and Digital circuit theoretically and practically. In the final stage of this internship, student will be able to design & execute a mini project.

Schedule: 05 days a week starting from 1st June 2025.

Course fee: Rs. 1000/- payable to 'student fund' A/C No. 10635508860

IFSC: SBIN0011812.

Coordinator1: Dr.Imteyaz Ahmad, Asso. Professor, ECE Dept., BIT Sindri

Contact: iahmad.ece@bitsindri.ac.in, 993298765 (Whatsapp)



Coordinator2: Prof. Minu Manjari, Assistant Professor, ECE Dept., BIT Sindri



Convener: Dr. A P Sinha, HOD ECE



Patron: Dr. Pankaj Rai, Director BIT Sindri



To register go to the link given below:

<https://forms.gle/De3iBCF6HHAqiQHg6>